IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

van der GOES et al.

Appl. No. To Be Assigned

(Continuation of Appl. No. 10/359,201;

Filed: February 6, 2003)

Filed: Herewith (July 24, 2003)

For: Subranging Analog to Digital

Converter with Multi-Phase Clock

Timing

Art Unit: To Be Assigned

Examiner: To Be Assigned

Atty. Docket: 1875.2820002/RES/GSB

Information Disclosure Statement

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Sir:

Listed on accompanying Form PTO-1449 are documents that may be considered material to the examination of this application, in compliance with the duty of disclosure requirements of 37 C.F.R. §§ 1.56, 1.97 and 1.98.

Where the publication date of a listed document does not provide a month of publication, the year of publication of the listed document is sufficiently earlier than the effective U.S. filing date and any foreign priority date so that the month of publication is not in issue. Applicants have listed publication dates on the attached PTO-1449 based on information presently available to the undersigned. However, the listed publication dates should not be construed as an admission that the information was actually published on the date indicated.

Applicants reserve the right to establish the patentability of the claimed invention over any of the information provided herewith, and/or to prove that this information may not

be prior art, and/or to prove that this information may not be enabling for the teachings purportedly offered.

This statement should not be construed as a representation that a search has been made, or that information more material to the examination of the present patent application does not exist. The Examiner is specifically requested not to rely solely on the material submitted herewith.

Applicants have checked the appropriate boxes below.

- □ 1. Statement under 37 C.F.R. 1.704(d). Each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application and this communication was not received by any individual designated in 37 C.F.R. § 1.56(c) more than thirty days prior to the filing of this information disclosure statement.
- □ 3. Filing under 37 C.F.R. § 1.97(c). This Information Disclosure Statement is being filed more than three months after the U.S. filing date AND after the mailing date of the first Office Action on the merits, but before the mailing date of a Final Rejection, or Notice of Allowance, or an action that otherwise closes prosecution in the application.
 - □ a. Statement under 37 C.F.R. § 1.97(e)(1). I hereby state that each item of information contained in this Information Disclosure Statement was

first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(1).

- □ b. Statement under 37 C.F.R. § 1.97(e)(2). I hereby state that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application and, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(2).
- □ c. Attached is our PTO-2038 Credit Card Payment Form [OR] Check No.

 _____ in the amount of \$ _____ in payment of the fee under 37

 C.F.R. § 1.17(p).
- □ 4. Filing under 37 C.F.R. § 1.97(d) This Information Disclosure Statement is being filed more than three months after the U.S. filing date and after the mailing date of a Final Rejection or Notice of Allowance, but before payment of the Issue Fee. Enclosed find our Check No. ______ in the amount of \$ _____ in payment of the fee under 37 C.F.R. § 1.17(p); in addition:
 - □ a. Statement under 37 C.F.R. § 1.97(e)(1). I hereby state that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(1).
 - □ b. Statement under 37 C.F.R. § 1.97(e)(2). I hereby state that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application and, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more

- 4 - Appl. No.: To Be Assigned (Continuation of Appl. No. 10/359,201)

than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(2).

- □ 5. The document(s) was/were cited in a search report by a foreign patent office in a counterpart foreign application. Submission of an English language version of the search report that indicates the degree of relevance found by the foreign office is provided in satisfaction of the requirement for a concise explanation of relevance. 1138 OG 37, 38.
- □ 6. A concise explanation of the relevance of the non-English language document(s) appears below:
- Zopies of the documents were cited by or submitted to the Office in an IDS that complies with 37 C.F.R. § 1.98(a)-(c) in Application No. 10/359,201, filed February 6, 2003, which is relied upon for an earlier filing date under 35 U.S.C. § 120. Thus, copies of these documents are not attached. 37 C.F.R. § 1.98(d).

It is respectfully requested that the Examiner initial and return a copy of the enclosed PTO-1449, and indicate in the official file wrapper of this patent application that the documents have been considered.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

Jeffrey T. Helvey Attorney for Applicants Registration No. 44,757

Date: 7/24/03

1100 New York Avenue, N.W. Washington, D.C. 20005-3934 (202) 371-2600

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	AT	1	mm²," IEEE	ult, Klaas and Buchwald, Aaron, "An Embedded 240-mW 10-b 50-MS/s CMOS ADC in 1-m²," IEEE Journal of Solid-State Circuits, IEEE, Vol. 32, No. 12, December 1997, p. 1887-1895.								
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	AT	3	1n 0.8 mm ²	oogzaad, G. and Roovers, R., "A 65-mW, 10-bit, 40-Msample/s BiCMOS Nyquist ADC n 0.8 mm²," <i>IEEE Journal of Solid-State Circuits</i> , IEEE, Vol. 34, No. 12, ecember 1999, pp. 1796-1802.								
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	AT	<u>5</u>	Lewis, S. of Solid-S	ewis, S. et al., "A 10-b 20-Msample/s Analog-to-Digital Converter," <i>IEEE Journal</i> f Solid-State Circuits, IEEE, Vol. 27, No. 3, March 1992, pp. 351-358.								
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	AS	<u>6</u>	End," IEEE	garaj, K. et al., "Efficient 6-Bit A/D Converter Using a 1-Bit Folding Front d," <i>IEEE Journal of Solid-State Circuits</i> , IEEE, Vol. 34, No. 8, August 1999, . 1056-1062.								
	AT	<u>6</u>	Converter	agaraj, K. et al., "A Dual-Mode 700-Msamples/s 6-bit 200-Msamples/s 7-bit A/D converter in a 0.25-μm Digital CMOS," <i>IEEE Journal of Solid-State Circuits</i> , IEEE, pl. 35, No. 12, December 2000, pp. 1760-1768.								
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	AT	7		g, W-C. et al., "A 10-b 20-Msample/s Low-Power CMOS ADC," <i>IEEE Journal of</i> id-State Circuits, IEEE, Vol. 30, No. 5, May 1995, pp. 514-521.								
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

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	AT	<u>9</u>	Wang, Y-T. Solid-Stat	ng, Y-T. and Razavi, B., "An 8-bit 150-MHz CMOS A/D Converter," <i>IEEE Journal of blid-State Circuits</i> , IEEE, Vol. 35, No. 3, March 2000, pp. 308-317.									
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